REMARKS

Claims 1-8 are pending in this application. By this Amendment, claims 1, 7 and 8 are amended. Claim 4 is canceled without prejudice to, or disclaimer of, the subject matter therein.

The Office Action rejects claims 1 and 2 under 35 U.S.C. § 102(b) over Ikeda et al. (U.S. Pat. No. 5,541,557); rejects claims 1, 2, and 4-8 under 35 U.S.C. § 102(b) over Fujimori (JP 11284441); rejects claims 1, 2, 4, 5, 7, and 8 under 35 U.S.C. § 102(b) over Ikeda (JP 04259104); and rejects claim 3 under 35 U.S.C. § 103(a) over Fujimori in view of Hatanaka et al. (U.S. Pat. No. 6,229,249). These rejections are respectfully traversed.

Claims 1, 7 and 8, from which claims 2, 3, 5 and 6 depend, recite, "a semiconductor device fixed onto a rear surface of the resonator package." It is respectfully submitted that Ikeda (557) does not disclose a semiconductor device fixed onto a rear surface of a resonator package. Ikeda (557) discloses a semiconductor 19 fixed to lead frame/tab 21, and not fixed to piezoelectric oscillator 18 (see Ikeda (557) at Figure 14(b) and col. 5, lines 45-51).

Regarding Fujimori, the temperature compensation quartz oscillator disclosed in Fujimori electrically and mechanically connects the terminal of an IC chip (4) to a circuit pattern of the oscillator directly. It is respectfully submitted that Fujimori does not disclose, teach or suggest a terminal arranged on a surface opposite to the surface mechanically joined to the rear surface of the piezoelectric oscillation package, as recited in the rejected claims. Similarly, it is respectfully submitted that Fujimori does not disclose, teach or suggest a semiconductor element and a terminal connected to the piezoelectric oscillation piece that are connected by wire bonding as recited in the rejected claims. Likewise, it is respectfully submitted that Fujimori does not disclose, teach or suggest the semiconductor element and an inner lead portion that are connected by wire bonding as recited in the rejected claims.

The subject matter recited in the rejected claims does not require a complex circuit pattern formed on a rear surface of the piezoelectric oscillation package. It is respectfully

Application No. 10/797,196

submitted that the subject matter disclosed in Fujimori does require a complex circuit pattern,

and thus lacks the advantages of the subject matter recited in the rejected claims.

Regarding Ikeda (104), the rejected claims recite a terminal arranged on a rear surface of

the piezoelectric oscillator. In contradistinction, piezoelectric oscillators (3, 22, 30, 54) of Ikeda

(104) have leads (5, 23, 33, 55) protruded from the side surface of the piezoelectric oscillators,

so the size of the piezoelectric oscillator becomes large.

For at least the foregoing reasons, it is respectfully requested that the rejections in the

Office Action be withdrawn.

In view of the foregoing, it is respectfully submitted that this application is in condition

for allowance. Favorable reconsideration and prompt allowance of claims 1-8 are earnestly

solicited.

Should the Examiner believe that anything further would be desirable in order to place

this application in even better condition for allowance, the Examiner is invited to contact the

undersigned at the telephone number set forth below.

Respectfully submitted,

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Date: December 14, 2005

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